

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	)	
	)	Examiner: Such, Matthew W.
<b>Hareland, Scott A., et al.</b>	)	
	)	Art Unit: 2891
Application No: 10/607,769	)	
	)	Atty Docket No.: 42P15685
Filed: June 27, 2003	)	
	)	Confirmation No.: 6439
For: NONPLANAR SEMICONDUCTOR	)	
DEVICE WITH PARTIALLY OR FULLY	)	
WRAPPED AROUND GATE ELECTRODE	)	
<u>AND METHODS OF FABRICATION</u>	)	

**Mail Stop Issue Fee**

Assistant Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT AFTER ALLOWANCE**

In accordance with Applicant's duty of disclosure, this Information Disclosure Statement, substitute for form 1449A/PTO (based on PTO/SB/08A), and cited reference(s) are being submitted prior to issuance of this application. It is respectfully requested that the cited reference(s) be placed in the file and that the subject application not be withdrawn from issuance.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is authorized to charge any deficiencies or credit any overpayments in connection with this submission to Deposit Account No. 02-2666, and is requested to notify us of same.

Respectfully submitted,  
**BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN, LLP**

Date: October 27, 2008

/Justin K. Brask/

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